



FLEX LOGIX OPENS UP LICENSING ACCESS TO ITS INFERX AI TECHNOLOGY

New strategic approach will accelerate market adoption of Flex Logix reconfigurable computing architecture, across multiple platforms

MOUNTAIN VIEW, Calif. – October 25, 2022 – Flex Logix® Technologies, Inc., a leading innovator in AI inference IP and the leading supplier of eFPGA IP, today announced that it has opened access to its edge inference AI solutions. Available in early 2023, device manufacturers and systems companies who design chips can now license Flex Logix's InferX AI technology, enabling broader access to the company's eFPGA and edge inference IP solutions.

Over the past four years, Flex Logix has advanced its InferX technology from initial design to 16nm production, with superior performance per dollar and watt, demonstrating a fundamentally advantaged inference architecture. The ecosystem of SoC customers today utilizing Flex Logix's EFLX eFPGA now stands to benefit from the availability of AI inference hardware and software IP for SoC integration.

"As commercial adoption of eFPGA accelerates, with more companies building superior products based on Flex Logix technology, we are pleased to also offer our customers AI IP along with a high efficiency automated compiler," said Geoff Tate, Co-Founder and CEO at Flex Logix. "We have nearly 40 chips under license utilizing our EFLX eFPGA IP, with more than 20 of them working in silicon, all first time. Flex Logix's license partners are poised to make reconfigurable computing ubiquitous over the coming decades – through both embedded FPGA and edge inference applications."

"Flex Logix has proven it can move from strength to strength, powering its partner ecosystem through IP licensing," said Peter Hebert, Co-Founder and Managing Partner of Lux Capital. "Lux, alongside our partners at Mithril Capital and Eclipse, continues to invest to help Flex Logix become the Arm for reconfigurable computing."

"There are a very small number of people in the world that have built multi-billion dollar companies in semiconductor IP – founding Rambus CEO and now Flex Logix CEO Geoff Tate is one of them," said Ajay Royan, Founder and Managing General Partner of Mithril Capital, which led the company's Series D round of financing. "Flex Logix's decision to open up access to its InferX technology will drive compelling performance and cost benefits to more end users,

sooner. We are strong supporters of the company's more aggressive market penetration strategy."

About Flex Logix

Flex Logix is a reconfigurable computing company providing leading edge eFPGA and AI Inference technologies for semiconductor and systems companies. Flex Logix eFPGA enables volume FPGA users to integrate the FPGA into their companion SoC, resulting in a 5-10x reduction in the cost and power of the FPGA and increasing compute density which is critical for communications, networking, data centers, microcontrollers and others. Its scalable AI inference is the most efficient, providing much higher inference throughput per square millimeter and per watt. Flex Logix supports process nodes from 180nm to 7nm; and can support other nodes on short notice. Flex Logix is headquartered in Mountain View, California and has an office in Austin, Texas. For more information, visit <https://flex-logix.com>.

####

MEDIA CONTACTS

Kelly Karr

Tanis Communications

kelly.karr@taniscomm.com

+408-718-9350

Copyright 2022. All rights reserved. Flex Logix and EFLX are registered trademarks of Flex Logix, Inc.